Call for Tutorials

Aim of the Conference:
IEEE DTS 2020 Conference will feature a state-of-the-art set of Tutorial presentations. The Organizing Committee invites proposals and ideas for tutorials to be held as part of the Conference. Tutorials will provide a good opportunity for conference attendees to learn from leaders in the field, explore cutting-edge topics, and acquire new insights, knowledge and skills. The topic should be of sufficient relevance with respect to the general scope of the conference. Potential organizers are invited to submit a brief (one-page) proposal and addressed to the Tutorial Chairs.

The topic should be of sufficient relevance with respect to the general scope of the conference:

- Analog, digital, mixed, and RF circuits design
- SoC, MPPSoC, NoC, SIP, and NIP design
- Embedded electronics and System Architecture
- MEMS, NEMS and MOEMS systems design
- Low-power electronics and systems design
- Sensors and Actuators
- Testing
- ATE issues
- Alternatives test strategies

Tutorial Chairs:
- Pr. Mourad Fakhfakh, ENETCOM, Tunisia
- Pr. Fakhreddine Ghaffari, ENSEA, France

Each Tutorial proposal should describe concisely the content, importance and timeliness of the tutorial. It should contain the following sections:

- Tutorial’s title.
- Tutorial’s type (introductory, intermediate, or advanced)
- Summary describing the main focus and topics to be addressed and explained why the tutorial is appropriate for DTS.
- A detailed outline of the presentation.
- A description of the proposer’s lecturing expertise.
- Proposed duration of the tutorial.

Deadline: Tutorial proposals due: January 20th, 2020

Conference Website: http://www.DTS-conf.org